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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10071298	FILING DATE 02/07/2002	CLASS 361	SUBCLASS 58	GAU 2836	EXAMINER BENENSON
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\*\*APPLICANTS: Belgacem Haba;

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\*\*CONTINUING DATA VERIFIED:  
THIS APPLICATION IS A CIP OF 09/564,064 05/03/2000

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	9797-0074-999
Verified and Acknowledged Examiners's initials		
TITLE : Semiconductor module with serial bus connection to multiple dies		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED:</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G
Assistant Examiner		<b>DRAWING</b>	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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